Refine Search

Search Results -

Terms	Documents
dock$3 same (port near10 (media or medium or disk or disc))	209

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L1

Refine Search

Recall Text Clear Interrupt

Search History

DATE: Tuesday, April 12, 2005 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L1	dock$3 same (port near10 (media or medium or disk or disc))	209	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
dock$3 same (port near10 (media or medium or disk or disc))	18

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
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Search: L2

Refine Search

Recall Text | Clear | Interrupt

Search History

DATE: Tuesday, April 12, 2005 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L2	dock$3 same (port near10 (media or medium or disk or disc))	18	L2
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L1	dock$3 same (port near10 (media or medium or disk or disc))	209	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(398/118 |398/140 |370/400 |370/402 |370/902 |370/907 |370/813 |370/913 |709/227 |709/249 |709/223 |709/236 |710/1 |710/104 |710/105 |710/303 |710/304 |710/100 |710/62 |710/72).ccls.	13913

Database: US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L3

Refine Search

Recall Text | Clear | Interrupt

Search History

DATE: Tuesday, April 12, 2005 Printable Copy Create Case

Set Name **Query**

side by side

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L3 710/1,104,105,303,304,100,62,72;709/227,249,223,236;370/400,402,902,907,813,913;398/118,1

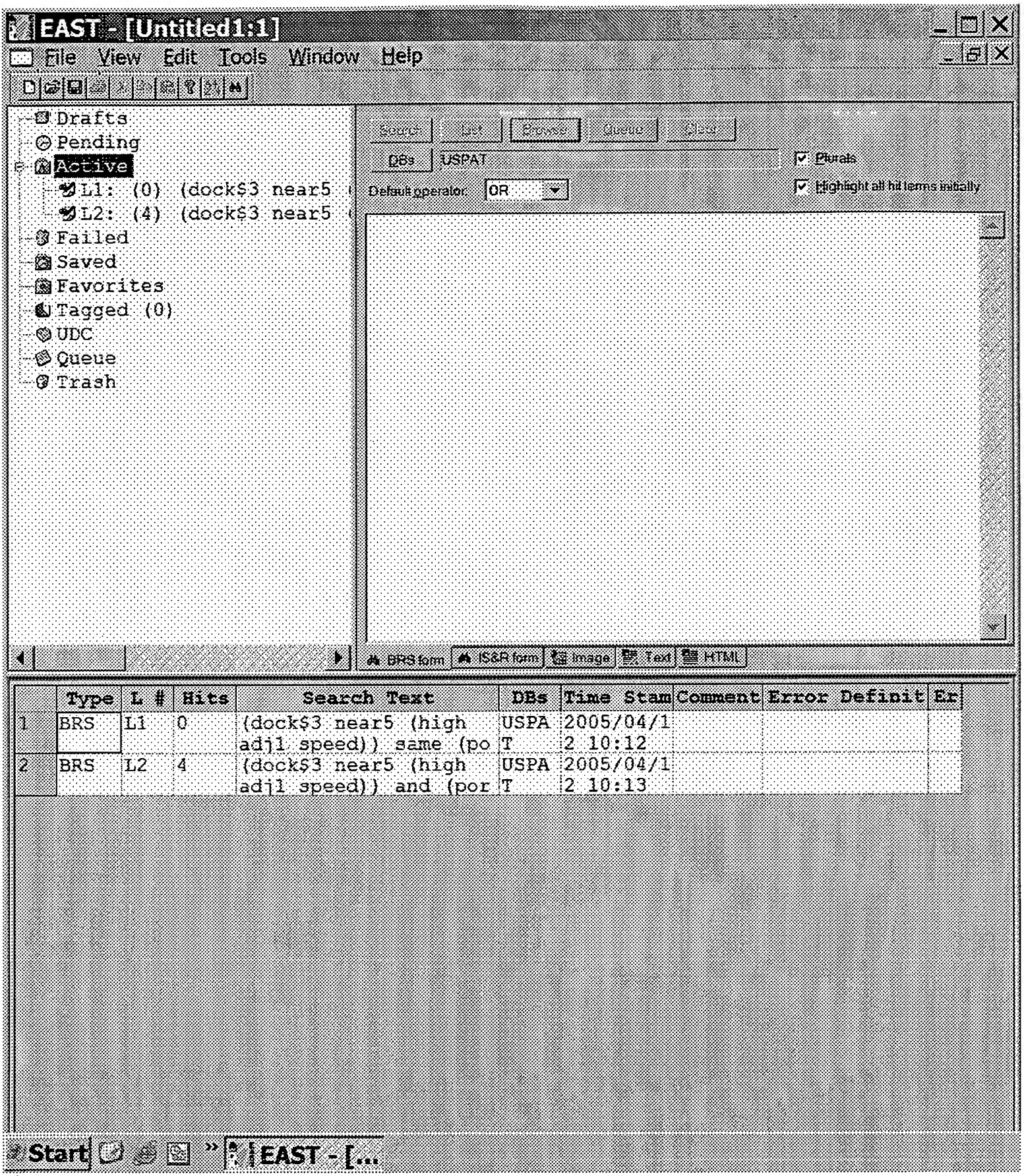
DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L2 dock$3 same (port near10 (media or medium or disk or disc))

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L1 dock$3 same (port near10 (media or medium or disk or disc))

END OF SEARCH HISTORY


EAST - [Untitled1:1]
File View Edit Tools Window Help
Drafts
Pending
Active
L1: (0) (dock$3 near5
L2: (4) (dock$3 near5
Failed
Saved
Favorites
Tagged (0)
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Queue
Trash
Search
Get
Browse
Queue
Clear
DBs
USPAT
Plurals
Default operator:
OR
Highlight all hit terms initially
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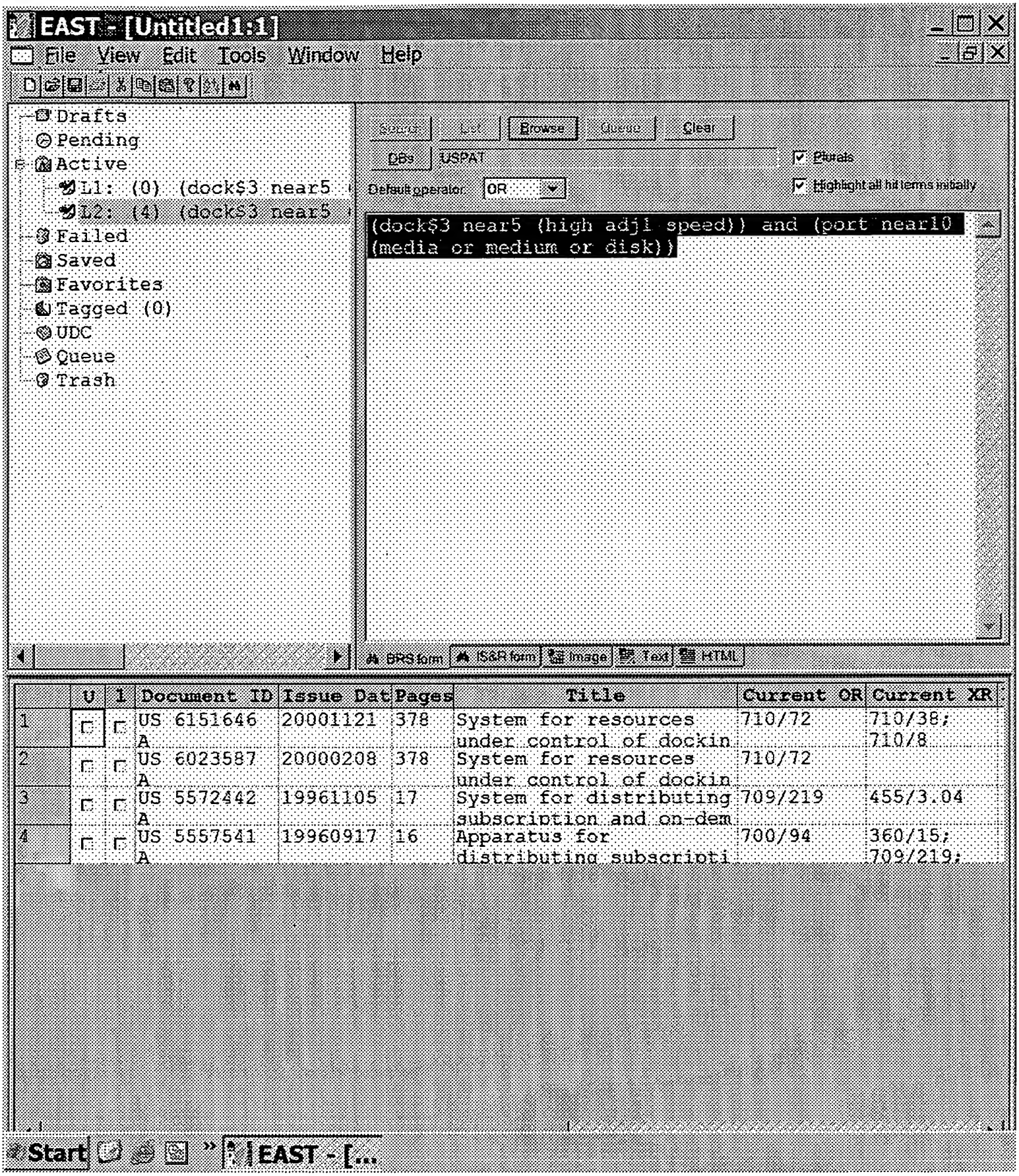
IS&R form
Image
Text
HTML
Type L # Hits Search Text DBs Time Stam Comment Error Definit Er
1 BRS L1 0 (dock$3 near5 (high adj1 speed)) same (po USPA T 2005/04/1 2 10:12
2 BRS L2 4 (dock$3 near5 (high adj1 speed)) and (por USPA T 2005/04/1 2 10:13
Start
EAST - [...


EAST - [Untitled1:1]
File View Edit Tools Window Help
Drafts
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L1: (0) (dock$3 near5
L2: (4) (dock$3 near5
Failed
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(dock$3 near5 (high adj1 speed)) and (port near10 (media or medium or disk))
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Title
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Current XR
1 US 6151646 A 20001121 378 System for resources under control of dockin 710/72 710/38; 710/8
2 US 6023587 A 20000208 378 System for resources under control of dockin 710/72
3 US 5572442 A 19961105 17 System for distributing subscription and on-dem 709/219 455/3.04
4 US 5557541 A 19960917 16 Apparatus for distributing subscripti 700/94 360/15; 709/219;
Start
EAST - [...

Results for "(dock*<in>metadata) <and> (port<in>metadata)"

e-mail

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A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» View Session History

» New Search

Modify Search

(dock*<in>metadata) <and> (port<in>metadata)

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

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Display Format: Citation Citation & Abstract

Select Article Information

1. **Topological representation and analysis method for multi-port and multi-orientation docking modular**
Ko, A.; Lau, T.L.; Lau, H.Y.K.;
Robotics and Automation, 2004. Proceedings. ICRA '04. 2004 IEEE International Conference on
Volume 3, 26 April-1 May 2004 Page(s):2210 - 2215 Vol.3
AbstractPlus | Full Text: PDF(622 KB) IEEE CNF

2. **Manipulator system for module re docking on the Mir Orbital Complex**
Syromiatnikov, V.S.;
Robotics and Automation, 1992. Proceedings., 1992 IEEE International Conference on
12-14 May 1992 Page(s):913 - 918 vol.1
AbstractPlus | Full Text: PDF(380 KB) IEEE CNF

3. **Experimental study of airflow and particle characteristics of a 300-mm POUP/LPU minienvironment sy**
Shih-Cheng Hu; Tzong-Ming Wu;
Semiconductor Manufacturing, IEEE Transactions on
Volume 16, Issue 4, Nov. 2003 Page(s):660 - 667
AbstractPlus | References | Full Text: PDF(481 KB) IEEE JNL

4. **Innovative Tools for Management: Real-Time Oceanographic Measurements and Circulation Modeling**
Frey, H.;
OCEANS
Volume 19, Sep 1987 Page(s):1091 - 1096
AbstractPlus | Full Text: PDF(544 KB) IEEE CNF

5. **A router architecture for real-time communication in multicomputer networks**
Rexford, J.; Hall, J.; Shin, K.G.;
Computers, IEEE Transactions on
Volume 47, Issue 10, Oct. 1998 Page(s):1088 - 1101
AbstractPlus | References | Full Text: PDF(500 KB) IEEE JNL

6. **Extended AEI applications and integration into on-dock intermodal information and operations netwo**
Mehlberg, U.;
TransTech Conference, 1995. Proceedings, 1995 Pacific Rim
30 July-2 Aug. 1995 Page(s):107 - 112
AbstractPlus | Full Text: PDF(508 KB) IEEE CNF

7. **Professor Silicon and Professor Maestro-the perfect combination**
Crynes, B.L.; Hawley, J.A., III;
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Volume 1, 1-4 Nov. 1995 Page(s):2a3.14 - 2a3.21 vol.1

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Ribeiro, M.I.; Lima, P.; Aparicio, P.; Ferreira, R.;
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Volume 2, 6-10 Oct. 1997 Page(s):973 - 976 vol.2
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9. **Planning and design of floating berths for passenger-only ferry terminals**
Joque, D.T.; Yang, F.L.; Demich, L.R.;
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Volume 2, 13-16 Sept. 1999 Page(s):848 - 861 vol.2
AbstractPlus | Full Text: PDF(1248 KB) IEEE CNF

10. **The effects of regulating the use of TBT-based antifouling paints on TBT contamination**
Evans, S.M.; Smith, R.;
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Volume 3, 13-16 Sept. 1999 Page(s):1119 - 1122 vol.3
AbstractPlus | Full Text: PDF(240 KB) IEEE CNF

11. **The Solid Rocket Booster Dewatering Set**
Fishel, K.;
OCEANS
Volume 11, Sep 1979 Page(s):681 - 684
AbstractPlus | Full Text: PDF(400 KB) IEEE CNF

A router architecture for real-time communication in multicomputer network

Rexford, J. Hall, J. Shin, K.G.
Network & Distributed Syst., AT&T Bell Labs., Florham Park, NJ, USA;

This paper appears in: **Computers, IEEE Transactions on**
Publication Date: Oct. 1998
Volume: 47 , Issue: 10
On page(s): 1088 - 1101
ISSN: 0018-9340
CODEN: ITCOB4
INSPEC Accession Number:6087354
DOI: 10.1109/12.729792
Posted online: 2002-08-06 22:11:47.0

Abstract
Parallel machines have the potential to satisfy the large computational demands of real-time applications. These applic predictable communication network, where time-constrained traffic requires bounds on throughput and latency, while g performance suffices for best-effort packets. This paper presents a new router architecture that tailors low-level routing, flow-control, and deadlock-avoidance policies to the conflicting demands of each traffic class. The router implements b and deadline-based scheduling, with packet switching and table-driven multicast routing, to bound end-to-end delay an for time-constrained traffic while allowing best-effort traffic to capitalize on the low-latency routing and switching scheme parallel machines. To limit the cost of servicing time-constrained traffic, the router includes a novel packet scheduler th scheduling logic across the multiple output **ports**, while masking the effects of **dock** rollover on the representation of p and deadlines. Using the Verilog hardware description language and the Epoch silicon compiler, we demonstrate that t the performance goals of both traffic classes in a single-chip solution. Verilog simulation experiments on a detailed timi show how the implementation and performance properties of the packet scheduler scale over a range of architectural p

Index Terms
Inspec

Controlled Indexing
multiprocessor interconnection networks packet switching parallel architectures performance evaluation systems

Non-controlled Indexing
Epoch silicon compiler Verilog hardware description language Verilog simulation architectural parameters requirements deadline-based scheduling end-to-end delay latency multicomputer networks packet swit parallel machines predictable communication network real-time applications real-time communication ro architecture table-driven multicast routing throughput time-constrained traffic

Author Keywords
Not Available

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On page(s): 1215- 1227, Volume: 49, Issue: 11, Nov 2000
Abstract | Full Text: PDF (376)

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Results for "((dock* and port)<in>metadata)" e-mail

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» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

((dock* 'and' port)<in> metadata)

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☐ 1. Document ID: EP 1341348 A1

Using default format because multiple data bases are involved.

L2: Entry 1 of 18 File: EPAB Sep 3, 2003

PUB-NO: EP001341348A1
DOCUMENT-IDENTIFIER: EP 1341348 A1
TITLE: Switch architecture independent of type of media

PUBN-DATE: September 3, 2003

INVENTOR-INFORMATION:

NAME	COUNTRY
KADAMBI, SHIRI	US
AMBE, SHEKHAR	US
RELAN, SANDEEP	IN

INT-CL (IPC): H04 L 12/44; H04 L 12/46
EUR-CL (EPC): H04L012/56; H04L029/06

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

☐ 2. Document ID: EP 1235222 A2

L2: Entry 2 of 18 File: EPAB Aug 28, 2002

PUB-NO: EP001235222A2
DOCUMENT-IDENTIFIER: EP 1235222 A2
TITLE: System for hard disk drive library

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

☐ 3. Document ID: EP 836133 A2

L2: Entry 3 of 18 File: EPAB Apr 15, 1998

PUB-NO: EP000836133A2
DOCUMENT-IDENTIFIER: EP 836133 A2
TITLE: Computer instrument docking apparatus

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

☐ 4. Document ID: US 20040260843 A1

L2: Entry 4 of 18 File: DWPI Dec 23, 2004

DERWENT-ACC-NO: 2005-073583
DERWENT-WEEK: 200508

TITLE: Semiconductor package for use as bridge between computer and peripheral device card e.g. memory cards, has areas of attribute memory and bus decode logic, where internal bridging-device-bus interconnects memory, logic, and bus

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

☐ 5. Document ID: US 20040228622 A1

L2: Entry 5 of 18 File: DWPI Nov 18, 2004

DERWENT-ACC-NO: 2005-029076
DERWENT-WEEK: 200503

TITLE: Docking station for supporting video system, has port that connects media source e.g. MPEG player, to station, where data from source is provided to station via port and to video system via connector

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

☐ 6. Document ID: US 6767253 B1

L2: Entry 6 of 18 File: DWPI Jul 27, 2004

DERWENT-ACC-NO: 2004-613013
DERWENT-WEEK: 200459

TITLE: Media-content-receiving component docking system for use with set-top box, has docking base comprising multiple communication ports transmits media content to receiving device without using receiving component

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

☐ 7. Document ID: US 20040257919 A1, DE 20313001 U1

L2: Entry 7 of 18 File: DWPI Dec 23, 2004

DERWENT-ACC-NO: 2004-170758
DERWENT-WEEK: 200504

TITLE: Combination unit for computer systems has a compact disc drive and card readers built into a standard module that fits into a computer port

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

☐ 8. Document ID: EP 1341348 A1, US 20030163625 A1

L2: Entry 8 of 18 File: DWPI Sep 3, 2003

DERWENT-ACC-NO: 2003-802080
DERWENT-WEEK: 200375

TITLE: Network switch for communication system, includes master which handles and processes data received by media ports of different media type through high speed docking station

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw. De

☐ 9. Document ID: US 6558049 B1

L2: Entry 9 of 18 File: DWPI May 6, 2003

DERWENT-ACC-NO: 2003-531324
DERWENT-WEEK: 200350

TITLE: Video stream processing system for portable computer, has video multiplexer which converts multiple video streams received through video input terminals, into single multiplexed video stream for output to graphics controller

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw. De

☐ 10. Document ID: US 6672332 B2, US 20030075219 A1

L2: Entry 10 of 18 File: DWPI Jan 6, 2004

DERWENT-ACC-NO: 2003-420788
DERWENT-WEEK: 200411

TITLE: Adjustable vertical pressure regulator used in paint ball gun, has coil spring disposed for cooperating engagement with piston to bias piston and insert into opened state and control flow of pressurized gas

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw. De

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Terms	Documents
dock$3 same (port near10 (media or medium or disk or disc))	18

Display Format: - Change Format

Previous Page Next Page Go to Doc#

L2: Entry 8 of 18 File: DWPI Sep 3, 2003

DERWENT-ACC-NO: 2003-802080
DERWENT-WEEK: 200375

TITLE: Network switch for communication system, includes master which handles and processes data received by media ports of different media type through high speed docking station

INVENTOR: AMBE, S; KADAMBI, S ; RELAN, S

PATENT-ASSIGNEE: BROADCOM CORP (BROAN)

PRIORITY-DATA: 2002US-0079576 (February 22, 2002)

Search Selected Search ALL Clear

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
EP 1341348 A1	September 3, 2003	E	000	H04L012/44
US 20030163625 A1	August 28, 2003		008	G06F013/00

DESIGNATED-STATES: AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LI LT LU LV MC MK NL PT RO SE SI SK TR

APPLICATION-DATA:

PUB-NO	APPL-DATE	APPL-NO	DESCRIPTOR
EP 1341348A1	February 19, 2003	2003EP-0003744	
US20030163625A1	February 22, 2002	2002US-0079576	

INT-CL (IPC): G06 F 13/00; H04 L 12/44; H04 L 12/46

ABSTRACTED-PUB-NO: US20030163625A
BASIC-ABSTRACT:

NOVELTY - A master (101) receives data from multiple media ports (103) through high speed docking stations (102) and processes the data received from multiple media ports.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for method of handling data in network switch.

USE - For handling data received from media ports of different media types in communication system.

ADVANTAGE - Provides maximum flexibility while handling data in network switch. The cost associated with the network switch is optimized.

DESCRIPTION OF DRAWING(S) - The figure shows the schematic view of the network

switch.

master 101

high speed docking stations 102

media ports 103

packet lanes 104

ABSTRACTED-PUB-NO: US20030163625A
EQUIVALENT-ABSTRACTS:

CHOSEN-DRAWING: Dwg.1/2

DERWENT-CLASS: W01
EPI-CODES: W01-A03B; W01-A06E1; W01-A06G2; W01-A06G5A;

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L6: Entry 1 of 3 File: PGPB Aug 28, 2003

PGPUB-DOCUMENT-NUMBER: 20030163625
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030163625 A1

TITLE: Switch architecture independent of media

PUBLICATION-DATE: August 28, 2003

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APPL-NO: 10/ 079576 [PALM]
DATE FILED: February 22, 2002

INT-CL: [07] G06 F 13/00

US-CL-PUBLISHED: 710/303
US-CL-CURRENT: 710/303

REPRESENTATIVE-FIGURES: 1

ABSTRACT:

A network device for handling data and a method for handling data in a network device are disclosed. The network device includes at least one media port and at least one high speed docking station, communicating with the at least one media port. At least one master is provided in the network device, where the at least one master is connected to the at least one high speed docking station. The master is configured to handle and process data received by the at least one media port and passed to the master through the at least one high speed docking station. The network device is configured to handle media ports of different media types. Thus, the device can handle data received through different media ports that have different media types with the same master, making the network device easily configured to meet a customer's needs.

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US006151646A

United States Patent [19]
Watts et al.

[11] Patent Number: 6,151,646
[45] Date of Patent: Nov. 21, 2000

[54] **SYSTEM FOR RESOURCES UNDER CONTROL OF DOCKING STATION WHEN STAND ALONE AND RESOURCES UNDER CONTROL OF CENTRAL PROCESSING UNIT OF PORTABLE COMPUTER WHEN DOCKED**

[75] Inventors: LaVaughn P. Watts, Temple; John C. Linn, Richmond, both of Tex.

[73] Assignee: Texas Instruments Incorporated, Dallas, Tex.

[*] Notice: This patent is subject to a terminal disclaimer.

[21] Appl. No.: 09/304,939

[22] Filed: May 4, 1999

Related U.S. Application Data

[63] Continuation of application No. 08/851,265, May 2, 1998.

[51] Int. Cl.[7] G06F 9/00

[52] U.S. Cl. 710/72; 710/38; 710/8

[58] Field of Search 710/69, 72, 38, 710/8; 714/25; 364/400.01; 708/1, 280.2; 395/872

[56] References Cited

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Primary Examiner—Thomas C. Lee
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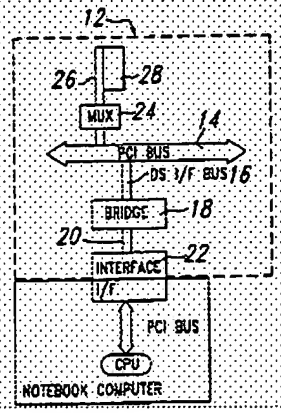
[57] ABSTRACT

The described embodiments of the present invention provide a computer docking station (12, 32, 58, 68, 76, 84, 90, 92, 94, 96) that can have its functionality reconfigured by a docked portable personal computer (10, 36, 62, 66, 74, 82). In at least one embodiment of the invention, the computer docking station is configured as a stand alone computer prior to docking with a portable computer, may have its functionality reconfigured when docked to the portable computer, and reconfigures itself to be a stand alone computer when undocked from the portable computer. In one embodiment of the invention, docking station resources are placed under the control of a docked portable computer. In another embodiment of the invention, docked portable computer resources are placed under the control of the docking station. The invention contemplates docking via direct connection, radio frequency "RF" communications, infrared "IR" communications, 1394 high performance serial bus communications, or card bus communications, and/or combinations of one or more of these communicative techniques.

12 Claims, 328 Drawing Sheets



US005557541A

United States Patent [19]

Schulhof et al.

[11] Patent Number: 5,557,541

[45] Date of Patent: Sep. 17, 1996

[54] **APPARATUS FOR DISTRIBUTING SUBSCRIPTION AND ON-DEMAND AUDIO PROGRAMMING**

[75] Inventors: Nathan Schulhof, Los Gatos; James M. Jaszky, Los Altos, both of Calif.

[73] Assignee: Information Highway Media Corporation, Cupertino, Calif.

[21] Appl. No.: 279,244

[22] Filed: Jul. 21, 1994

[51] Int. Cl.[6] G11B 20/10

[52] U.S. Cl. ..... 364/514 R; 341/7

[58] Field of Search ..... 364/514 R; 348/7, 348/6, 13; 360/19.1, 15; 455/4.2; 370/60.1; 358/342

[56] **References Cited**

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Primary Examiner—Emanuel T. Voeks
Assistant Examiner—Thomas Peeso
Attorney, Agent, or Firm—Michael A. Glenn

[57] **ABSTRACT**

A distribution system for audio program materials includes a portable audio storage and retrieval device that is programmable from a high speed data transfer system, and that includes a high-capacity data storage medium, a base control interface for identifying and accepting program material, a mobile control interface for displaying the identity of recorded material for playback selection, a recording mechanism for accepting very high speed digital data from an external source at rates faster than real time, and a playback mechanism for retrieving the stored data from the storage medium and for converting the data into audio signals for playback.

6 Claims, 7 Drawing Sheets